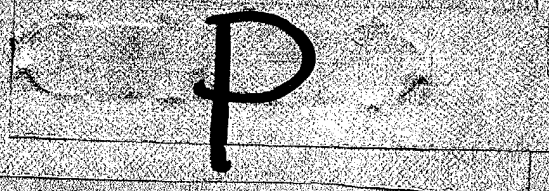


505-778



	Class	Subclass	ISSUE CLASSIFICATION



PATENT NUMBER
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U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED L112 0A 11/2	

APPLICATION NO. 09/918739	CONT/PRIOR D	CLASS 438	SUBCLASS 666	ART UNIT 2827 2014	EXAMINER D. GRAYBILL
APPLICANTS David Corisis					
TITLE Leads under chip in conventional IC package					

PTO-2040  
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
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